



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/054,653 Confirmation No.: 9448
First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002
Group Art Unit: 2814 Examiner: Farahani, D.
Atty. Docket No.: NS-5127 US
Title: Gate-Enhanced Junction Varactor With Gradual
Capacitance Variation
Assignee(s): National Semiconductor Corporation

Mountain View, California
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AMENDMENT TO TEXT UNDER 37 CFR 1.312

Sir:

Responsive to the Notices of Allowance and Allowability mailed 18 October 2005
and under the provisions of 37 CFR 1.312, please amend the above patent application as
described below.

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